

**IN THE CLAIMS:**

**Amend claims 1 and 2 as shown on the following pages  
8-10.**

1. (Currently Amended) A circuit, for use in testing an IC-module; said circuit comprising:

a single transistor having a current channel which is coupled in series with a first resistor between a source voltage terminal and an output terminal;

said single transistor also having a control lead, for enabling and disabling the flow of current ~~[[thru]]~~ through said current channel, which receives an input test signal;

a second resistor which couples said output terminal to a reference voltage terminal; and,

a socket which is structured to hold said IC-module, having a test signal input terminal that is coupled to said output terminal.

2. (Currently Amended) A circuit according to claim 1 wherein said first and second resistors have magnitudes which limit the square of said current ~~[[thru]]~~ through said current channel, times said first and second resistors, to be less than one-tenth of one watt.

3. (Original) A circuit according to claim 1 wherein said first resistor is connected directly to said source voltage terminal, and said current channel of said single transistor is connected directly to said output terminal.

4. (Original) A circuit according to claim 1 wherein said current channel of said single transistor is connected directly to said source voltage terminal, and said first resistor is connected directly to said output terminal.

5. (Original) A circuit according to claim 1 wherein said single transistor has a resistance through said current channel when said current is enabled which has a large tolerance, and said first and second resistors are substantially larger in resistance and have a small tolerance.

6. (Original) A circuit according to claim 1 which further includes a variable voltage source which has an output that is coupled to said source voltage terminal.

7. (Original) A circuit according to claim 1 wherein the only coupling between said output terminal and said second resistor is a conductor.

8. (Original) A circuit according to claim 1 wherein the only coupling between said output terminal and said second resistor is a conductor in a series with an inductive filter.

9. (Original) A circuit according to claim 1 wherein the only coupling between said output terminal and said test signal input terminal on said socket is a conductor and an input to one digital multiplexor.

10. (Original) A circuit according to claim 1 wherein the only coupling between said output terminal and said test signal input terminal on said socket is a conductor and an input to one analog multiplexor.

11. (Original) A circuit according to claim 1 wherein said single transistor is an N-channel field effect transistor.

12. (Original) A circuit according to claim 1 wherein said single transistor is a P-channel field effect transistor.